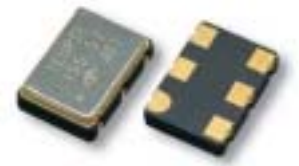


VT Type For PECL/LVDS Voltage Controlled Crystal Oscillator

RoHS Compliant Standard

FEATURE

1. Typical 7.0 x 5.0 x 1.6mm 6 Pads ceramic SMD package.
2. Tight symmetry (45 to 55%) available.
3. Wide frequency control range.
4. Low phase jitter.
5. Complementary output.
6. Grounded cover for reduced EMI.
7. Packing: Tape & Reel, 1000/3000 pcs per Reel, 1~99 pcs per Bulk/Tape.



Actual Size

ORDERING INFORMATION

V	T	E	S	P	C	L	-	N	F	-	?
VCXO	Package (mm)	Supply Voltage(V)	Tri-State Function	Freq.Stability /Pulling Range (ppm)	Temp. Range (°C)	Output Logic and Symmetry	Dash	Appearance	Lead Free	Dash	Freq.(MHz)
	7x5	J: 2.5 E: 3.3	S: Enable Low	M: ±25/±70 P: ±50/±70	I: -10~+60 C: -20~+70 L: -40~+85	L: PECL / 50±5% V: LVDS / 50±5%		N: Normal	F: RoHS Compliant		xx.xxxxxx

Ordering Example: VTESPCL-NF-155.520000 MHz

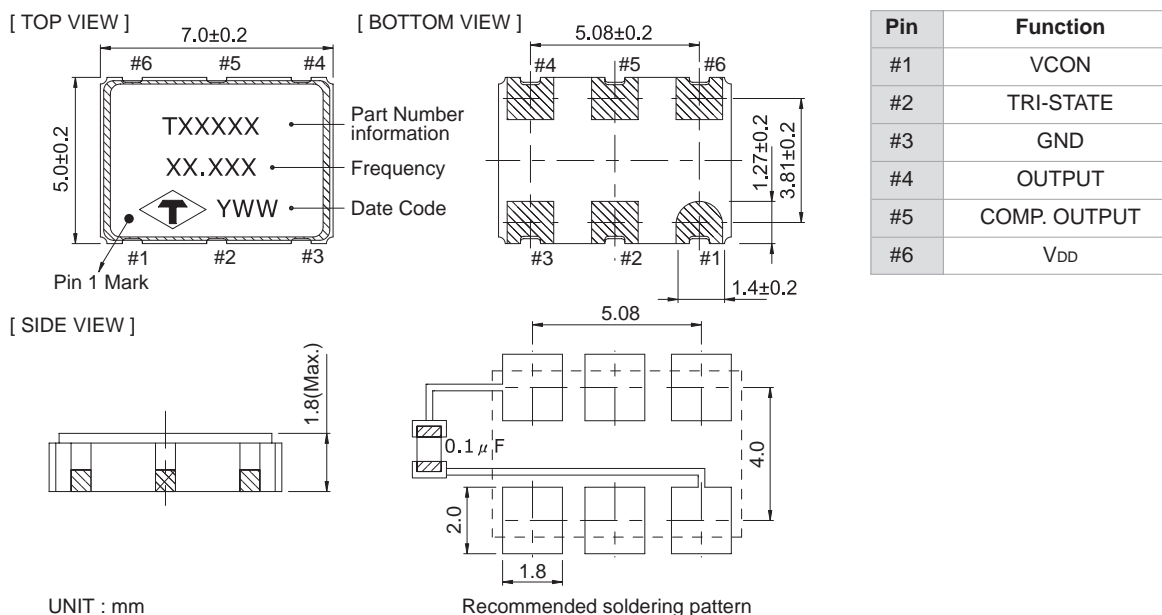
VCXO T-TYPE; V_{DD}: 3.3V; Tri-State Enable Low; Freq. Stability: ±50ppm, Pulling Range: ±70ppm; Temp Range: -20°C to +70°C; PECL Output Symmetry: 50±5%; Noemal Appearance; RoHS Compliant; Freq. 155.520000MHz.

FREQ. STABILITY vs. TEMP. RANGE

Temp.(°C)	ppm	M: ±25	P: ±50
I	-10 ~ +60	△	○
C	-20 ~ +70	△	○
L	-40 ~ +85	X	○

○ : Standard △ : Available (case by case)
 × : Not available

OUTLINE DRAWING



UNIT : mm

ELECTRICAL SPECIFICATION

Output Logic Parameter	PECL				LVDS				Unit
	Min.		Max.		Min.		Max.		
	3.3	2.5	3.3	2.5	3.3	2.5	3.3	2.5	V
Supply Voltage Variation(V _{DD}) 5%	3.135	2.375	3.465	2.625	3.135	2.375	3.465	2.625	V
Frequency Range	1.5	65	200	200	1.5	65	200	200	MHz
Standard Frequency	19.44、 76.8、 77.76、 106.25、 122.88、 125、 155.52、 156.25								MHz
Operating Temp. Range	Refer to Ordering Information								°C
Frequency Stability *	Refer to Ordering Information								ppm
Pulling Range	±70		—		±70		—		ppm
Supply Current									
1.5MHz Fo < 65MHz	—		75		—		45		mA
65MHz Fo 200MHz	—		100		—		80		
Output Level									
Output High (Logic "1")	2.275	1.475	—		—		1.6		V
Output Low (Logic "0")	—		1.68	1.095	0.9		—		
Transition Time:Rise/Fall Time*	—		1.0		—		1.0		nSec
Start Time	—		3		—		3		mSec
Tri-State									
Output Active	—		0.5	0.5	—		0.5	0.5	V
Output in High Impedance State	2.5	2	—		2.5	2	—		
Linearity	—		20		—		20		%
RMS Phase Jitter (Integrated 12KHz~20MHz)	—		1		—		1		pS
Storage Temp. Range	-55		125		-55		125		°C

*Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

*Transition times are measured between 20% and 80% waveform.