

VK Type Voltage Controlled Crystal Oscillator

RoHS Compliant Optional

FEATURE

1. Typical (L)14.3 x (W) 8.7 x (max. H)6.0mm.
2. Pulling range: ± 500 ppm max.(Optional)
3. Aging: ± 1 ppm/year.
4. TTL/CMOS output.
5. Packing: Tape & Keel, 500 pcs per Reel, 1~99 pcs per Bulk/Reel.



ORDERING INFORMATION

V	K	G	U	P	C	J	-	N	L	-	?
VCXO	Package (mm)	Supply Voltage (V)		Freq.Stability/Tolerance/Pulling Range(ppm)	Temp. Range (°C)	Output Logic and Symmetry	Dash	Appearance	Lead Free	Dash	Freq.(MHz)
	14.3x8.7	SMD G: 5.0 F: 3.3		M: $\pm 25/\pm 15/\pm 100$ G: $\pm 35/\pm 20/\pm 100$ P: $\pm 50/\pm 20/\pm 100$ R: $\pm 50/\pm 20/\pm 150$ T: $\pm 25/\pm 15/\pm 150$ K: $\pm 50/\pm 20/\pm 200$	C: -20~+70 D: -30~+80 L: -40~+85	50 \pm 5% 50 \pm 10% 10TTL 15pF CMOS 15pF CMOS 50pF		N:Normal	F:RoHS Compliant L:Not RoHS Compliant		xx.xxxxxx

Ordering Example: VKGUPCJ-NL-10.000000 MHz

VCXO K-TYPE; V_{DD} : 5V; Freq. Stability: ± 50 ppm; Freq. Tolerance: ± 20 ppm, Pulling Range: ± 100 ppm; Temp. Range: -20°C to +70°C; CMOS 15pF Duty:50 \pm 5%; Normal Appearance; Not RoHS Compliant; Freq. 10.000000MHz.

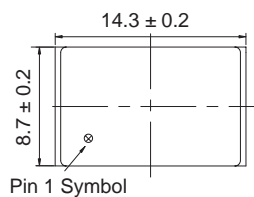
FREQ. STABILITY vs. TEMP. RANGE

Temp°C	Freq Stab/Tolerance/Pulling	M: $\pm 25/\pm 15/\pm 100$	G: $\pm 35/\pm 20/\pm 100$	P: $\pm 50/\pm 20/\pm 100$	R: $\pm 50/\pm 20/\pm 150$	T: $\pm 25/\pm 15/\pm 150$	K: $\pm 50/\pm 20/\pm 200$
C	-20~ +70	△	○	○	○	△	○
D	-30~ +80	X	○	○	○	X	○
L	-40~ +85	X	○	○	○	X	△

○ : Standard △ : Available (case by case) × : Not available

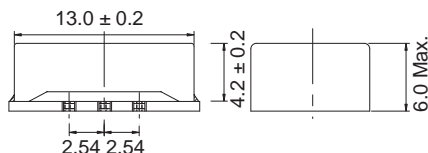
OUTLINE DRAWING

[TOP VIEW]

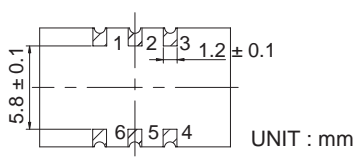


Pin	Function
1	VCON
2	TRI-STATE (or Null)
3	GND
4	OUTPUT
5	NC (or Null)
6	V_{DD}

[SIDE VIEW]



[BOTTOM VIEW]



ELECTRICAL SPECIFICATION

Parameter	Min.		Max.		Unit
	5.0	3.3	5.0	3.3	
Supply Voltage Variation(V_{DD}) 5%	4.75	3.13	5.25	3.47	V
Frequency Range	2.5		45		MHz
Operating Temp. Range	Refer to Ordering Information				°C
Frequency Stability *	Refer to Ordering Information				ppm
Frequency Stability					
Vs Supply Voltage(±5%) change	—		±3		ppm
Vs Load(±10%) change	—		±3		
Vs Aging	—		±1		ppm/year
Supply Current					
2.5000MHz Fo < 10.000MHz	—		15	12	mA
10.000MHz Fo < 15.000MHz	—		22	20	
15.000MHz Fo < 26.000MHz	—		30	28	
26.000MHz Fo < 45.000MHz	—		35	33	
Output Level (TTL/CMOS)					
High Level("1")	90% V _{DD} or 2.4V		—		V
Low Level ("0")	—		10% V _{DD} or 0.4V		V
Duty	40%		60%		
Vc Input Impedance	50				MΩ
Start Time	—		2		mSec
Storage Temp. Range	-55		125		°C

*Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.