

# VT Type For PECL /LVDS Multiplier Voltage Controlled Crystal Oscillator

RoHS Compliant Standard

## FEATURE

1. Typical 7.0 x 5.0 x 1.6mm 6 Pads ceramic SMD package.
2. Tight symmetry (45 to 55%) available.
3. Wide frequency control range.
4. Low phase jitter.
5. Complementary output.
6. Grounded cover for reduced EMI.
7. Packing: Tape & Reel, 1000/3000 pcs per Reel, 1~99 pcs per Bulk/Tape.



## ORDERING INFORMATION

V	T	E	M	P	C	L	-	N	F	-	?
VCXO	Package (mm)	Supply Voltage(V)	Tri-State Function	Freq.Stability /Pulling Range (ppm)	Temp. Range (°C)	Output Logic and Symmetry	Dash	Appearance	Lead Free	Dash	Freq.(MHz)
	7x5	E: 3.3	M:Multiplier (PLL) with Tri-State	M:±25/±100 P:±50/±100	I : -10~+60 C: -20~+70 L: -40~+85	L:PECL / 50±5% V:LVDS / 50±5%		N:Normal	F:RoHS Compliant		xx.xxxxxx

### Ordering Example: VTEMPCL-NF-155.520000 MHz

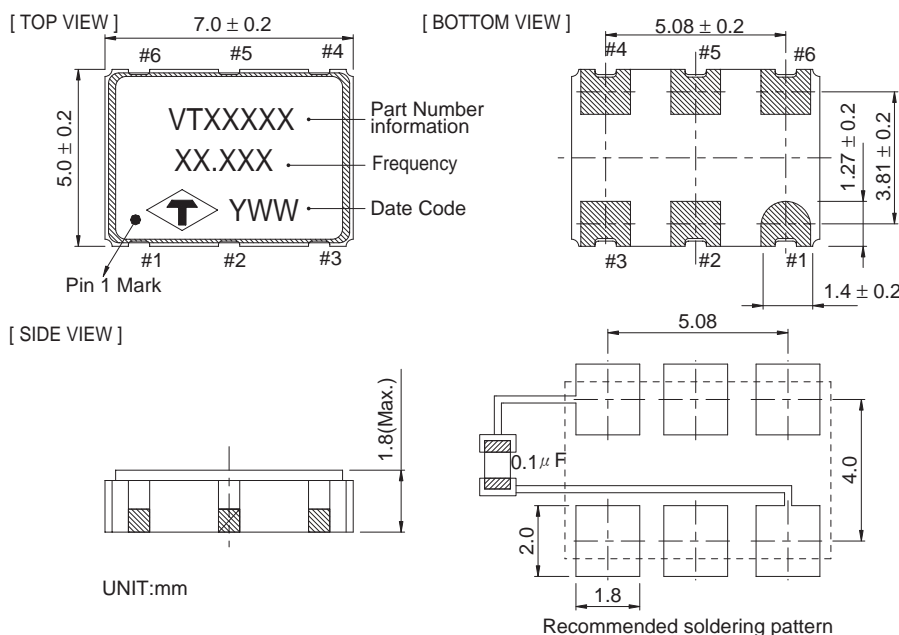
VCXO T-TYPE; V<sub>DD</sub>: 3.3V; Multiplier (PLL) with Tri-State; Freq. Stability: ±50ppm; Pulling Range:±100ppm; Temp. Range: -20°C to +70°C; PECL Symmetry: 50±5%; Noemal Appearance; RoHS Compliant; Freq. 155.520000MHz.

## FREQ. STABILITY vs. TEMP. RANGE

Temp.(°C)	ppm	M: ±25	P: ±50
I	-10 ~ +60	○	○
C	-20 ~ +70	△	○
L	-40 ~ +85	X	○

○ : Standard    △ : Available (case by case)  
X : Not available

## OUTLINE DRAWING



Pin	Function
#1	VCON
#2	TRI-STATE
#3	GND
#4	OUTPUT
#5	COMP. OUTPUT
#6	V <sub>DD</sub>

## ELECTRICAL SPECIFICATION

Output Logic Parameter	PECL		LVDS		Unit
	Min.	Max.	Min.	Max.	
	3.3		3.3		V
Supply Voltage Variation(V <sub>DD</sub> ) 10%	2.97	3.63	2.97	3.63	V
Frequency Range	100	700	100	700	MHz
Standard Frequency	125、200、400、622.08、625				MHz
Operating Temp. Range	Refer to Ordering Information				°C
Frequency Stability *	Refer to Ordering Information				ppm
Pulling Range	±100	—	±100	—	ppm
Supply Current					
100MHz Fo<160MHz	—	75	—	65	mA
160MHz Fo 700MHz	—	100	—	80	
Output Level					
Output High (Logic "1")	2.275	—	—	1.6	V
Output Low (Logic "0")	—	1.68	0.9	—	
Transition Time:Rise/Fall Time*	—	1.0	—	1.0	nSec
Start Time	—	3	—	3	mSec
Tri-State					
Output Active	2.5	—	2.5	—	V
Output in High Impedance State	—	0.5	—	0.5	
Linearity	—	20	—	20	%
RMS Phase Jitter (Integrated 12KHz~20MHz)	—	4	—	4	pS
Storage Temp. Range	-55	125	-55	125	°C

\*Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

\*Transition times are measured between 20% and 80% waveform.